



2182 #2 BT

PATENT
P6157
0045.0022

11-04-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Examiner: Not Yet Assigned
J.S. Taylor)
Serial No.: 10/066,362)
Filed: January 31, 2002) , Art Unit: 2182
For: METHOD, SYSTEM, PROGRAM,)
AND DATA STRUCTURE FOR)
IMPLEMENTING A LOCKING)
MECHANISM FOR A SHARED)
RESOURCE)

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Assistant Commissioner of Patents
Washington, D.C. 20231


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application. Applicants submit that no fee is needed nor should be charged with the submittal of this Information Disclosure Statement (IDS) because this IDS is being submitted under 1.97(b) in that the IDS is being filed before the mailing date of a first Office Action on the merits or within three months of the filing date of a national application other than a continued prosecution application under 1.53(d). Applicants submit that a first Office Action has not been received in the above referenced case.

Nonetheless, should any fee be deemed necessary, please charge any insufficiency or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this letter is enclosed for fee processing.

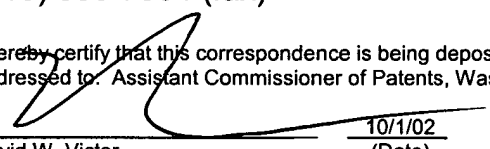
Respectfully submitted,


David W. Victor
Reg. No. 39,867

Dated: October 1, 2002

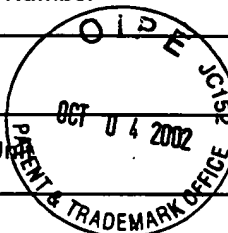
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on October 1, 2002.

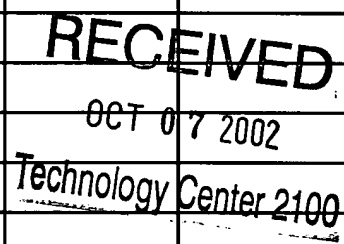

David W. Victor

10/1/02
(Date)

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>								Docket Number P6157		Application Number 10/066,362	
								Applicant J.S. Taylor			
								Filing Date January 31, 2002		Group Art Unit 2182	



U.S. PATENT DOCUMENTS												
EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE



FOREIGN PATENT DOCUMENTS												
	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>	
	Lamport, L. "The Mutual Exclusion Problem: Part I-A Theory of Interprocess Communication". <i>Journal of the Association for Computing Machinery</i> , Vol. 33, No. 2, April 1986, pp. 313-326.
	Ford, D. "Mutual Exclusion and Synchronization: Mimicking Win32 synchronization mechanisms". [online] <i>Dr. Dobb's Journal</i> , January 1998, [Retrieved on October 17, 2001]. Retrieved from the Internet at <URL: http://www.ddj.com/articles/1998/9801/9801g/9801g.htm >
	Lamport, L. "The Mutual Exclusion Problem: Part I-A Theory of Interprocess Communication". <i>Journal of the Association for Computing Machinery</i> , Vol. 33, No. 2, April 1986, pp. 327-348.
	Herlihy, M. and J.E.B Moss. "Transactional Memory: Architectural Support for Lock-Free Data Structures". Copyright Digital Equipment Corporation and J.E.B. Moss, December 1, 1992. pp. 1-40.
	Kramer, D. "The Java Platform", Copyright Sun Microsystems, Inc., May 1996. pp. 1-24.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.